Student Workshop Abstract Template

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This is a sample latex template for abstracts submitted to the 1995 Student Workshop on Scalable Computing. The workshop will be sponsored by the MIT Laboratory for Computer Science to promote an interchange of ideas among the various research activities at MIT related to scalable computing. This year's student workshop will be entirely organized by students. Students — undergraduate, graduate, and postdoctoral — are encouraged to submit short, 2-page abstracts of recent technical accomplishments. All submitted abstracts will be bound in a volume to be distributed at the workshop and which will later be available as an LCS technical report. About 18 of the submitted abstracts will be selected for presentation.

Please follow these instructions carefully. Submit a single-sided original (for reproduction) and 15 double-sided and 3-hole punched copies (for committee evaluation) of a 2-page abstract that briefly describes the technical contributions of the work. The first page *must* begin with a title, the author's name, and MIT and email addresses. Joint submissions by students are acceptable, but the student making the presentation should be listed as first author. Faculty coauthorship is not allowed, although student-written abstracts based on joint work with faculty members are permitted. The abstract should describe the essential ideas in the research and have a short bibliography that includes any published papers or technical memoranda on which the abstract is based. Every abstract should have at least one figure, table, diagram, or photograph. A footnote on the bottom of the first page should identify the student's faculty supervisor and acknowledge research support.

Figure 1 is an example of a figure which is one column in width. Figure 2 is an example of a figure which is two columns in width. These are cites for a book [Lei92], a conference paper [L+92], a journal paper [Fis81], a technical report [Lei89], a thesis [Bre91], and an unpublished manuscript [CDG+93].

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References

[Bre91] Eric A. Brewer. Aspects of a high-performance parallel-architecture simulator. Master's thesis, MIT, September 1991.

[CDG+93] David Culler, Andrea Dusseau, Seth Goldstein, Avind Krishnamurthy, Steven Lumetta, Thorsten von Eicken, and Katherine Yelic. Introduction to split-c. Version 1.0, April 1993.

[Fis81] Joseph Fisher. Trace scheduling: A technique for global mi-

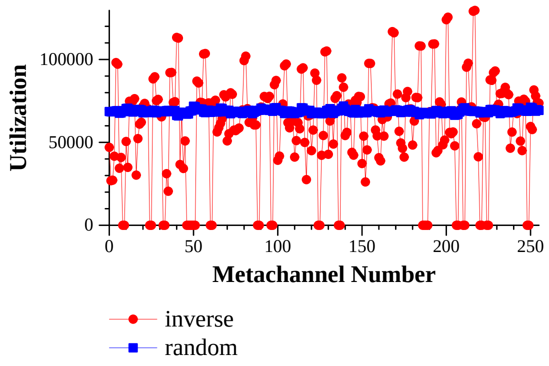

Figure 2: A double-column figure.

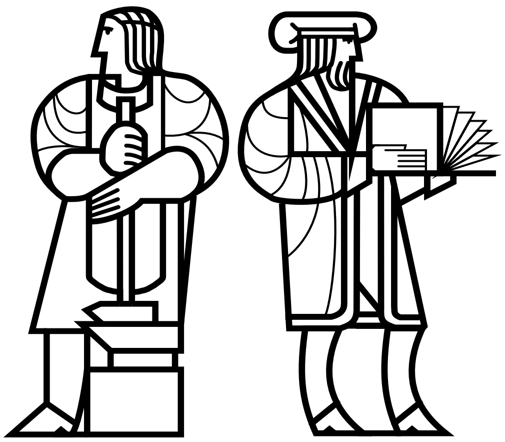

Figure 1: A single-column figure.

crocode compaction. *IEEE Transactions on Computers*, 30(7):478–490, July 1981.

[L+92] Charles E. Leiserson et al. The network architecture of the connection machine CM-5. In *Symposium on Parallel Architectures and Algorithms*, pages 272–285, San Diego, California, June 1992. ACM.

[Lei89] Charles E. Leiserson. Vlsi theory and parallel supercomputing. MIT/LCS/TM 402, MIT, 545 Technology Sq., Cambridge, MA 02139, May 1989. Also appears as an invited presentation at the 1989 Caltech Decennial VLSI Conference.

[Lei92] F. T. Leighton. *Introduction to parallel algorithms and architectures*. Morgan Kaufmann, San Mateo, CA, 1992.